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Dzi	AR	1	MIPS IV Instruction Set (R10000), Revision 3.2, MIPS Technologies, Inc., entire publication submitted (September, 1995).						
DTL	AS	1	MIPS RISC	MIPS RISC Architecture - Volume I - Introduction to the ISA, MIPS Technologies, Inc., entire publication submitted (August, 1998).					
Da	АТ	1	MIPS RISC	Architecture - Volun	ne II - The Instruction Set, MIPS Technol	ogies, Inc., entin	e publicati	on submitted (Au	gust, 1998).
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Page 2 of 6 ATTY, DOCKET NO. APPLICATION NO. 09/364,786 0077.20 (1778.0120002) **FORM PTO-1449 APPLICANT** Thekkath et al. INFORMATION DISCLOSURE STATEMENT **FILING DATE GROUP** July 30, 1999 2672 **U.S. PATENT DOCUMENTS EXAMINER CLASS** SUB-CLASS INITIAL **DOCUMENT** DATE NAME FILING DATE NUMBER AA2 AB2 AC2 AD2 AE2 Technology Center 2600 AF2 AG2 AH2 AI2 AJ2 AK2 **FOREIGN PATENT DOCUMENTS EXAMINER** INITIAL DOCUMENT DATE COUNTRY **CLASS** SUB-CLASS TRANSLATION NUMBER Yes AL2 No Yes No AM2 Yes AN2 No Yes No AO2 Yes AP2 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Kubosawa, H. et al., "Four-way VLIW Geometry Processor for 3D Graphics Applications," FUJITSU Sci. Tech. J., 36, 1, pp.39-47 (June AR 2 2000) (manuscript received December 1, 1999). DIC Thekkath et al., "An Architecture Extension for Efficient Geometry Processing," slides 1-23, presented at Hot Chips 11, A Symposium DIL AS 2 of High-Performance Chips, Stanford Univ. (August 1999) (submitted for conference review July 14, 1999). Uhler, M., "Optimizing Game Applications for the MIPS RISC Architecture," Computer Game Developer's Conference, San Jose, CA, ΑT 2 1771 14 pages (March 1999) (submitted for conference review on 2/12/99). **EXAMINER** DATE CONSIDERED Danrl **EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.

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Dri	AS	3	AltiVec™ Te	chnology Fact Shee	ef, Motorola, Inc., 2 pages (1998).				
670	AT	3	AltiVec™ Te	chnology Programr	ming Environments Manual, Rev. 0.1, Mo	otorola, Inc., enti	ire documen	t submitted (Nov	vember 1998).

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DZC	AS	<u>4</u>	Fuller, S., *Moto	uller, S., "Motorola's AltiVec <sup>™</sup> Technology" [white paper], Motorola, Inc., 4 pages (1998).					
Psc	AT	4	Diefendorff et al	Diefendorff et al., "AltiVec Extension to PowerPC Accelerates Media Processing," IEEE Micro, pp. 85-95 (March/April 2000).					
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AltiVec<sup>™</sup> Technology Programming Environments Manual, Preliminary Rev. 0.2, Motorola, Inc., pp. i-viii, xi-xviii, 1-1 thru 1-11, 4-16 thru 4-33, 6-82, 6-122, 6-135, and 6-143 (May 1998). AR 5 D7(

3DNow!™ Technology Manual, Advanced Micro Devices, Inc., entire document submitted (72 pages total) (May 1998). AŞ <u>5</u> DZI

Copy of Office Action from U.S. Patent Application No. 09/249,177, filed February 12, 1999, 10 pages, mailed June 21, 2000. ΑT <u>5</u> DSC

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### U.S. PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT DATE NAME CLASS SUB-CLASS FILING DATE NUMBER AA6 AB6 AC6 OCT 2 8 2003 AD6 AE6 Technology Center 2600 AF6 AG6 AH6 Al6 AJ6 AK6 **FOREIGN PATENT DOCUMENTS EXAMINER** INITIAL DOCUMENT DATE COUNTRY CLASS SUB-CLASS **TRANSLATION** NUMBER AL6 No Yes AM6 No Yes AN6 No Yes A06 No Yes AP6 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Rice et al., "Multiprecision division on an 8-bit processor," Computer Arithmetic, 1997, Proceedings, 13th IEEE Symposium on 6-9 July AR 6 D>C 1997, pp. 74-81. AS <u>6</u>

AS 6 1997, pp. 74-81.

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